

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

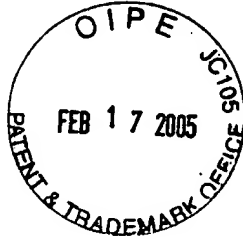
Applicants: Chainer et al.

Serial No.: 09/723,362

Filed: 11/27/2000

Title: METHOD AND APPARATUS FOR CORRECTING FOR SYSTEMATIC  
ERRORS IN TIMING PATTERN GENERATION

To: Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attention: Official Draftsman



Confirmation No.: 6942

Group Art Unit: 2651

Examiner: Andrew L. Snizek

Dear Sir:

Enclosed please find twenty-seven (27) replacement sheets of formal drawings with reference to the above entitled patent application.

Respectfully submitted,

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